

INITIAL INFORMATION DATA SHEET

APPLICATION INFORMATION

Application Type:: Regular
Subject Matter:: Utility
Title:: Method and System for Integrating
CORES in FPGA-Based System-On-Chip
(SoC)
Docket Number:: X-1002 US
Request for Early Pub?:: No
Request for Non-Pub?:: Yes
Total Drawing Sheets:: Six
Small Entity?:: No
Petition included?:: No

INVENTOR INFORMATION

Inventor Authority Type:: Inventor
Primary Citizenship Ctry:: US
Status:: Full Capacity
Given Name:: L. James
Family Name:: Hwang
Street:: 2120 Camino de los Robles
City:: Menlo Park
State or Province:: California
Postal or Zip Code:: 94025

Inventor Authority Type:: Inventor
Primary Citizenship Ctry:: US
Status:: Full Capacity
Given Name:: Reno L.
Family Name:: Sanchez
Street:: 8520 Beverly Hills
City:: Albuquerque
State or Province:: New Mexico
Postal or Zip Code:: 87122

CORRESPONDENCE INFORMATION

Correspondence Customer Number:: 24309

REPRESENTATIVE INFORMATION

Representative Customer Number::	24309	
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ASSIGNEE INFORMATION

Assignee Name:: Xilinx, Inc.
Street:: 2100 Logic Drive
City:: San Jose
State or Province:: California
Postal or Zip Code:: 95124